



Docket No. A5GN2442US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ahmed Mohamed Abdelatty Ali

Application No. 10/622,989

Examiner: Howard L. Williams

Filed: July 18, 2003

Art Unit: 2819

Title: ANALOG-TO-DIGITAL CONVERTER METHODS AND STRUCTURES
FOR INTERLEAVABLY PROCESSING DATA SIGNALS AND
CALIBRATION SIGNALS

Commissioner for Patents
Mail Stop Issue Fee
Post Office Box 1450
Alexandria, Virginia 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

Applicant agrees that the Examiner has accurately stated one reason for allowance of the claims. However, the claims include other patentable aspects that were not identified by the Examiner. This response is submitted to avoid the possibility of any unnecessary limitation on the scope of the claims which might result from the stated reason for allowance. It is being submitted concurrently with the issue fee payment.

Respectfully submitted,

May 26, 2004

Richard S. Koppel
Registration No. 26,475
Attorney for Applicant

KOPPEL, JACOBS, PATRICK & HEYBL
555 St. Charles Drive, Suite 107
Thousand Oaks, California 91360
(805) 373-0060

J:\D7\A5GN2442 Reasons for Allow